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PATENT APPLICATION

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

John Bruno

Examiner: Nathan W. Ha

Serial No.:

10/064,856

Art Unit: 2814

Filing Date:

August 23, 2002

Docket No.: 00100.02.0038

Title: INTEGRATED CIRCUIT HAVING MEMORY DISPOSED THEREON AND METHOD OF MAKING THEREOF

Mail Stop RCE Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

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Alexandria, VA 22313-1450, on this date:

1-19-06

Christine Wright

PRELIMINARY AMENDMENT

Dear Sir:

In response to the Final Office Action mailed July 19, 2005 and the Advisory Action mailed December 20, 2005, Applicant submits a Request for Continued Examination and the following preliminary amendment and response:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 7 of this paper.